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PATENT
09/997,786

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Duncan M. Walker, et al.
Serial No.: 09/997,786 ✓
Filing Date: November 30, 2001
Art Unit: 4798
Title: *SYSTEM AND METHOD FOR DETECTING QUIESCENT
CURRENT IN AN INTEGRATED CIRCUIT*

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

<p>EXPRESS MAIL"</p> <p>Express Mailing Label Number EV 324626628 US Date of Deposit November 10, 2003</p> <p>I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</p> <p><i>Willie Jiles</i> Willie Jiles</p>
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**PRELIMINARY AMENDMENT AND
RESPONSE TO RESTRICTION REQUIREMENT**

Prior to examination of this utility patent application entitled *System and Method for
Detecting Quiescent Current in an Integrated Circuit*, please amend the application as follows: